Electronic Acknowledgement Receipt

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Title of Invention:	SYSTEM AND METHOD FOR CONFIGURING A RECONFIGURABLE SYSTEM
First Named Inventor/Applicant Name:	Joseph E. Peck
Correspondence Address:	Jeffrey C. Hood, Esq. Meyertons, Hood, Kivlin, Kowert & Goetzel , P.C. P.O. Box 398 - Austin TX 78767-0398 US 5124761400 -
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	NPL Documents	B2_Real-Time_Workshop_part-2.pdf	23621138	no	115
Warnings:					
Information:					
2	NPL Documents	B3_Guide_to_Rapid_Prototyping_with_Simulink_Real-Time_Workshop_and_dSpace.pdf	6356937	no	16
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3	NPL Documents	B4_Real-Time_Interface_to_Simulink--RTI_30--Users_Guide.pdf	6293964	no	125
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4	NPL Documents	B5_GORMAN_Real-Time_Data_Aquisition_and_Controls_using_Matlab.pdf	1355513	no	4
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5	NPL Documents	B8_Alta_Group_of_Cadence_Design_Systems.pdf	8664189	no	34
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6	NPL Documents	B9_Code_Generation_System--product_data_sheet.pdf	1205079	no	8
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7	NPL Documents	B6_SPW-Matlab_Co-Simulation_Interface_Product_Data_Sheet.pdf	493590	no	2
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8	NPL Documents	B7_Signal_Processing_Work System--MatLab_Interface_User_Guide.pdf	18833215	no	72
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9	Petition for review by the Office of Petitions.	Petition.pdf	109439	no	3
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Information:					
10	Information Disclosure Statement (IDS) Filed	IDS.pdf	236409	no	5
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11	NPL Documents	B1_Choosing_Block-Diagram_Tools_for_DSP_Design.pdf	1239442	no	7
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12	NPL Documents	B2_Real-Time_Workshop_part-1.pdf	6892139	no	115
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13	NPL Documents	B13_Xanalog_Corporation_Sales_Manual.pdf	1337858	no	8
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14	NPL Documents	B17_Xanalog--The_CAE_Workstation_comes_to_Simulation.pdf	683370	no	3
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15	NPL Documents	B18_Xanalog-RT_Real_Time_Analog_and_Digital_I-O.pdf	829638	no	4
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16	NPL Documents	B19_Xanalog_SC-plus.pdf	922520	no	5
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17	NPL Documents	B10_SPW-CGS_Porting_Kits--product_data_sheet.pdf	523485	no	2
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18	NPL Documents	B11_MultiProx_for_SPW--product_data_sheet.pdf	580362	no	4

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19	NPL Documents	B12_DSP_ProCoder_for_SPW--product_data_sheet.pdf	1445990	no	4
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20	NPL Documents	B14_Available_XA-1000_Literature_and_its_use.pdf	170743	no	2
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21	NPL Documents	B15_XA-1000_Programming_ICONS.pdf	859391	no	15
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22	NPL Documents	B16_Xanalog_CAE_System--The_Fastest_AT_Alive.pdf	257255	no	1
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23	NPL Documents	B20_Xanalog_Specializing_in_Workstations_for_Continuous_Dynamic_Simulation.pdf	3606112	no	24
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24	NPL Documents	B21_XANALOG_Real-time_User_Guide.pdf	4826846	no	28
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25	NPL Documents	B22_LEE_Gabriel--A_Design_Environment_for_Programmable_DSPs.pdf	1434134	no	13
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26	NPL Documents	B23_LEE_A_Design_Tool_for_HW_and_SW_for_Multi-processor_DSP_Systems.pdf	682926	no	4
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28	NPL Documents	B25_BUCK_Scheduling_Dynamic_Dataflow_Graphs_with_Bounded_Memory.pdf	1015999	no	4
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29	NPL Documents	B26_LEE_Design_Methodology_for_DSP.pdf	28868	no	4
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30	NPL Documents	B27_PINO_Interface_Synthesis_in_Heterogeneous_System-Level_DSP_Design_Tools.pdf	1163055	no	4
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31	NPL Documents	B28_PINO_Software_Synthesis_Single-Processor_DSP_Systems_Using_Ptolemy.pdf	8820417	no	48
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33	NPL Documents	B30_PINO_Automatic_Code_Generation_for_Heterogeneous_Multiprocessors.pdf	1361302	no	4
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34	NPL Documents	B31_BINDRA_Tool_Chest_Continues_to_grow.pdf	821684	no	2
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35	NPL Documents	B32_PINO_Mapping_Multiple_Independent_Synchronous_Dataflow_Graphs.pdf	1813923	no	6
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36	NPL Documents	B33_KALAVADE_Hardware-Software_Co-Design_Using_Ptolemy.pdf	4336891	no	18

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37	NPL Documents	B34_PINO_Software_Synthesis_for_DSP_using_Ptolemy.pdf	3291896	no	15
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41	NPL Documents	B36_i-Logix_Product_Overview.pdf	6832731	no	44
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42	NPL Documents	B37_I-Logix_Statemate_MAGNUM_supports_PCs.pdf	581932	no	2
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43	NPL Documents	B38_I-Logix_Signs_Reseller_Agreement_for_VAPS.pdf	451944	no	2
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46	NPL Documents	B41_I-Logix_and_Integrated _Systems_Link_Statemate_ Magnum_and_Matrix_Autoc ode.pdf	286980	no	2
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47	NPL Documents	B42_I-Logix_and_Wind_Rive r_unveil_Industrys_First_Ra pid_Prototyping_Solution.pdf	299762	no	3
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Information:					
48	NPL Documents	B43_I-Logix-Inc_Endorses_ Unified_Modeling_Language .pdf	297398	no	1
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Information:					
	Total Files Size (in bytes):		197101779		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.